See IQAN–MD4 System drawing

Fuse

Emergency stop

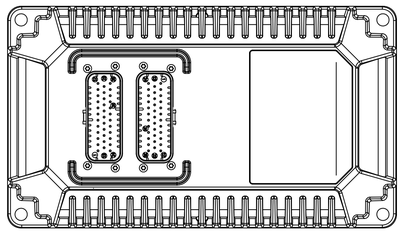
Main

Battery

+

–

IQAN–XC10



Only 1 ADDR pin is used.
Determined by address assigned to XC10 in your application.

Fuse 20A

–BAT

+BAT

+VREF

–VREF

+BAT –BAT

+BAT (or may use +VREF)

14 15 16 17 2 3 4 5 6 11 28 7 18 31 19 30 29 22 10 21 9 20 32 8

–BAT –BAT –BAT –BAT +BAT +BAT +BAT +BAT +BAT POWER CONTROL +VREF VIN A VIN B VIN C VIN D FIN A FIN B DIN A DIN B DIN C DIN D DIN E DIN F DIN G

Power supply | Voltage inputs | Freq. inputs | Digital inputs

22 18 30 19 31 20 32 33 21 13 25 2 26 3 27 16 17 28 29 34 35

–VREF VIN E VIN F VIN G VIN H VIN I VIN J FIN C FIN D DIN H DIN I DIN J DIN K DIN L DIN M DIN N DIN O DIN P ADDR.0 ADDR.1 ADDR.2

Voltage inputs | Freq. inputs | Digital inputs | ADDR

C1

PWM outputs

PWMOUT A+ PWMOUT A– PWMRET A PWMOUT B+ PWMOUT B– PWMRET B

Digital outputs

DOUT A DOUT B

CAN

CAN–H CAN–L

1 13 12 24 25 35 26 27 34 33

C2

PWM outputs

PWMOUT C+ PWMOUT C– PWMRET C PWMOUT D+ PWMOUT D– PWMRET D

Digital outputs

DOUT C DOUT D DOUT E DOUT F DOUT G DOUT H

4 5 1 6 7 24 8 9 10 11 12 23

Function A | Function B | Function C | Function D

Diode: 1N5404

CAN

–BAT

See IQAN–MD4 System drawing

Alternative I/O config. for PWMOUT Channels

C1

1 13 12 24 25 35

DOUT I DOUT J DOUT(LS) Q DOUT K DOUT L DOUT(LS) R

C2

4 5 1 6 7 24

DOUT M DOUT N DOUT(LS) S DOUT O DOUT P DOUT(LS) T

Diode: 1N5404

–BAT

PROTECTED +BAT SOURCE OR DOUT

ENGINEERING YOUR SUCCESS

Electrical schematic IQAN–XC10

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Information in this document is subject to change without notice